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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Seiji Yaegashi, et al.

Serial No. : 10/026,950

Art Unit : 2811

Filed : December 27, 2001

Examiner : To Be Assigned

For : HETEROJUNCTION BIPOLAR TRANSISTOR AND METHOD
OF MAKING HETEROJUNCTION BIPOLAR TRANSISTOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. 1.56, Applicants are enclosing an Information Disclosure Citation Form (PTO-1449) and a copy of the documents cited therein.

It is respectfully requested that the cited documents be considered by the Examiner in the above-identified patent application and that the cited documents be made officially of record therein. It is further requested that a listing of the same appear on the face of any patent which may issue from this application.

A first Official Action has not yet issued for this application. It is respectfully urged that no fees are required for the Examiner's consideration of the documents listed in this Information Disclosure Statement.

Respectfully submitted,
SMITH, GAMBRELL & RUSSELL, LLP

By: 

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